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## \*BIBDATASHEET\*

CONFIRMATION NO. 3784

Bib Data Sheet

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| SERIAL NUMBER<br>10/626,956 | FILING DATE<br>07/25/2003<br>RULE | CLASS<br>438 | GROUP ART UNIT<br>2813 | ATTORNEY DOCKET<br>NO.<br>Z&P-INF-P10709 |
|-----------------------------|-----------------------------------|--------------|------------------------|--|

## APPLICANTS

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none

C C

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
GERMANY 102 33 916.3 07/25/2002

C C

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/28/2003

| Foreign Priority claimed<br>35 USC 119 (a-d) conditions met             | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | STATE OR<br>COUNTRY<br>GERMANY | SHEETS<br>DRAWING<br>6 | TOTAL<br>CLAIMS<br>21 | INDEPENDENT<br>CLAIMS<br>2 |
|---|---|--------------------------------|------------------------|-----------------------|----------------------------|
| Verified and Acknowledged<br>Examiner's Signature<br><u>C. Claudare</u> | Initials  |                                |                        |                       |                            |

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## TITLE

Method for fabricating a vertical transistor, and semiconductor memory cell having a trench capacitor and an associated vertical selection transistor

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